

APR 25 2007

Application No.: 10/773,541

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Attorney Docket No.: 188122000700  
Client Ref. No.: CAD: 03-021  
(PATENT)REMARKS

Claims 1-20 are pending in the present application. By virtue of this response, claims 1 and 16 have been amended; no claims have been canceled or added. Accordingly, claims 1-20 are currently under consideration. Amendment and cancellation of certain claims is not to be construed as a dedication to the public of any of the subject matter of the claims as previously presented.

*Claim Rejections – 35 U.S.C. § 101*

Claims 1-13 and 16-20 are rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter.

In response, Applicants have amended claims 1, 13, and 16 to include the element of “storing the first set of model results in a memory device”. With this amendment, Applicants respectfully submit that the pending claims are directed to practical applications, which produce useful, concrete, and tangible results. For example, in one embodiment of the present application discloses a circuit simulator, which includes a model engine module (model engine) and a solver module (solver). The model engine includes device models for modeling the circuit elements and is programmed to produce model results associated with a circuit element based on the set of terminal biases for the circuit element supplied by the solver. The solver is programmed to form circuit equations in matrix format, to supply the set of terminal biases for each of a plurality of circuit elements represented in the circuit equations to the model engine, to receive the model results produced by the model engine, to stamp the model results into designated entries in matrices associated with the circuit equations, to check for correctness of the stamped model results by determining whether the model results interrelate according to a plurality of preset rules, and to solve the circuit equations to produce circuit simulation results. As a result, the method for evaluating a device model for a circuit element, as disclosed in the present application, improves upon conventional model quality assurance procedures, and produces better device models for circuit simulations of non-linear circuits.

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The support for the amendments to claims 1, 13, and 16 is found in Figure 1 and its corresponding descriptions in paragraphs [0016] and [0017].

For at least the reasons presented above, Applicants respectfully request the rejections under 35 U.S.C. 101 be withdrawn.

*Allowable Subject Matter*

Claims 14 and 15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

In response, based on the amendment to claim 13 and the reasons presented above, Applicants respectfully submit that claims 14 and 15 are in condition for allowance for at least the reason they depend from an allowable base claim 13.

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(PATENT)**CONCLUSION**

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to withdraw the outstanding rejection of the claims and to pass this application to issue. If it is determined that a telephone conference would expedite the prosecution of this application, the Examiner is invited to telephone the undersigned at the number given below.

In the event the U.S. Patent and Trademark office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952** referencing docket no. 188122000700. However, the Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: April 25, 2007

Respectfully submitted,



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